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Attorney Docket No.: 0180151

**SEP 06 2007**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: **Labelle, et al.**

Art Unit: 1765

Serial No.: 10/705,347

Examiner: Chen, Kin Chan

Filed: 11/08/2003

**For: Method for Integrating a High-K Gate  
Dielectric in a Transistor Fabrication  
Process**

**AMENDMENT AND RESPONSE TO NON-FINAL OFFICE ACTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir/Madam:

This is in response to the *Non-Final Office Action* dated June 1, 2007 in the above-referenced patent application. Please enter and consider the following amendments and remarks.